



DEMOCRATISING ACCELERATED COMPUTING WITH oneAPI

DATE: 08TH JUNE, 2023 (THURSDAY) | **TIME:** 09:30 AM - 04:30PM

VENUE: SERC AUDITORIUM, 4TH FLOOR, IISC, BANGALORE

This workshop is targeted for HPC users where they will be introduced to heterogeneous computing using Intel® GPU's and SYCL programming. Attendees will learn about oneAPI, its components, usage of compatibility tool to migrate from CUDA to SYCL source, usage of Intel® oneAPI DevCloud and SYCL programming details.

Attendees are required to pre-register with [Intel® oneAPI DevCloud \(https://devcloud.intel.com/oneapi/\)](https://devcloud.intel.com/oneapi/) before the workshop to be able to carry out hands-on exercises during the sessions.

| AGENDA | |
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| Topic | Presenter |
| Keynote | Alexis Crowell VP & GM for HPC/AI, Technical Resources SMG - Asia Pacific, Intel® |
| Intel® oneAPI overview | Vinutha S V Sr. Technical Consulting Engineer, Intel® |
| Intel® Data Center GPU Max series overview | |
| Getting started with the Intel® oneAPI DevCloud | |
| Introduction to SYCL concepts <ul style="list-style-type: none"> • Introduction to SYCL Programming • Program structure • Buffers & Accessors • Unified Shared Memory • Sub-Groups • Intel® oneAPI DPC++/C++ Compiler • Hands on lab | Subarnarekha Ghosal Technical Consulting Engineer, Intel® |
| Break | |
| Introduction to Intel® DPC++ Compatibility Tool (DPCT) <ul style="list-style-type: none"> • DPCT Usage • Hands on with DPCT using sample code • GPU performance case study | Vinutha S V Sr. Technical Consulting Engineer, Intel® |
| Q&A BYOC planning | |

Registration closes on 06th June, 2023

Register: <https://shorturl.at/kloPR>



SCAN QR CODE FOR REGISTER